

Vishay General Semiconductor

# **Surface Mount Ultrafast Plastic Rectifiers**



DO-214AC (SMA)

## FEATURES

- Low profile package
- Ideal for automated placement
- Glass passivated chip junction
- Ultrafast recovery times for high efficiency
- Low forward voltage, low power loss
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Solder dip 260 °C, 40 s
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

## **TYPICAL APPLICATIONS**

For use in secondary rectification and freewheeling for ultrafast switching speeds ac-to-ac and dc-to-dc converters in high temperature conditions for both consumer and automotive applications.

### **MECHANICAL DATA**

Case: DO-214AC (SMA)

Epoxy meets UL 94V-0 flammability rating

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD22-B102

E3 suffix for consumer grade, meets JESD 201 class 1A whisker test, HE3 suffix for high reliability grade (AEC Q101 qualified), meets JESD 201 class 2 whisker test

Polarity: Color band denotes cathode end

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	SYMBOL	ESH1B	ESH1C	ESH1D	UNIT	
Device marking code		EHB	EHC	EHD		
Maximum repetitive peak reverse voltage V <sub>RRM</sub> 100 150 200				200	V	
Maximum RMS voltage	V <sub>RMS</sub>	70	105	140	V	
Maximum DC blocking voltage	V <sub>DC</sub>	100	150	200	V	
Maximum average forward rectified current at $T_L$ = 150 °C	I <sub>F(AV)</sub>	1.0			А	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load (JEDEC Method)	I <sub>FSM</sub>	50			А	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	- 55 to + 175			°C	

PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub> 1 A					
V <sub>RRM</sub> 100 V, 150 V, 200 V					
t <sub>rr</sub>	25 ns				
V <sub>F</sub>	0.90 V				
T <sub>J</sub> max.	175 °C				



RoHS

COMPLIANT

# ESH1B, ESH1C & ESH1D

Vishay General Semiconductor



<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	VALUE	UNIT	
Maximum instantaneous forward voltage	I <sub>F</sub> = 0.7 A <sup>(1)</sup> I <sub>F</sub> = 1 A		V <sub>F</sub>	0.87 0.90	V	
Maximum DC reverse current at rated DC blocking voltage		T <sub>A</sub> = 25 °C T <sub>A</sub> = 125 °C	I <sub>R</sub>	1.0 25	μΑ	
Maximum reverse current	V <sub>R</sub> = 20 V, T <sub>J</sub> = 150 °C	•	I <sub>R</sub>	50	μA	
Maximum reverse recovery time	$I_{\rm F} = 0.5 \text{ A}, I_{\rm R} = 1 \text{ A}, I_{\rm rr} = 0.25 \text{ A}$		t <sub>rr</sub>	25	ns	
Typical reverse recovery time	$I_F = 0.6 \text{ A}, V_R = 30 \text{ V},$ dI/dt = 50 A/µs, $I_{rr} = 10 \% I_{RM}$	T <sub>J</sub> = 25 °C T <sub>J</sub> = 100 °C	t <sub>rr</sub>	25 35	ns	
Typical stored charge	$I_F = 0.6 \text{ A}, V_R = 30 \text{ V},$ dI/dt = 50 A/ $\mu$ s, $I_{rr} = 10 \% I_{RM}$	T <sub>J</sub> = 25 °C T <sub>J</sub> = 100 °C	Q <sub>rr</sub>	10 15	nC	
Typical junction capacitance	4.0 V, 1 MHz		CJ	25	pF	

#### Note:

(1) Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<b>THERMAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	OL ESH1B ESH1C ESH1D		UNIT		
Typical thermal resistance <sup>(1)</sup>	${\sf R}_{ heta {\sf J} {\sf A}} \ {\sf R}_{ heta {\sf J} {\sf L}}$	85 30		°C/W		

### Note:

(1) Units mounted on P.C.B. with 5.0 x 5.0 mm (0.013 mm thick) land areas

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
ESH1D-E3/61T	0.064	61T	1800	7" diameter plastic tape and reel	
ESH1D-E3/5AT	0.064	5AT	7500	13" diameter plastic tape and reel	
ESH1DHE3/61T (1)	0.064	61T	1800	7" diameter plastic tape and reel	
ESH1DHE3/5AT <sup>(1)</sup>	0.064	5AT	7500	13" diameter plastic tape and reel	

#### Note:

(1) Automotive grade AEC Q101 qualified

### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

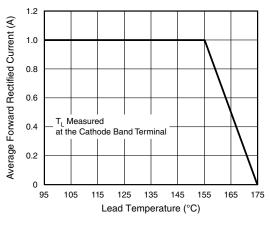


Figure 1. Maximum Forward Current Derating Curve

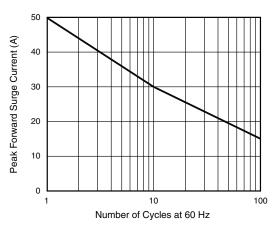


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current



# ESH1B, ESH1C & ESH1D

# Vishay General Semiconductor

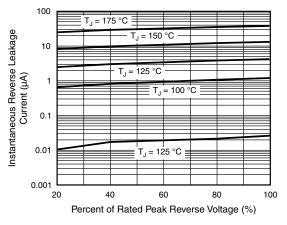


Figure 3. Typical Reverse Leakage Characteristics

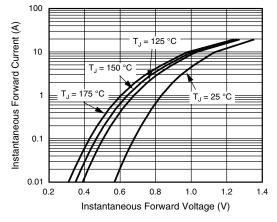


Figure 4. Typical Instantaneous Forward Characteristics



DO-214AC (SMA)

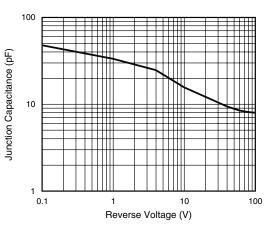


Figure 5. Typical Junction Capacitance

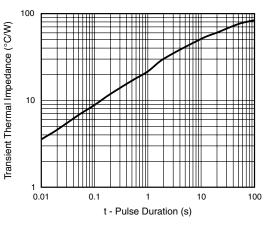
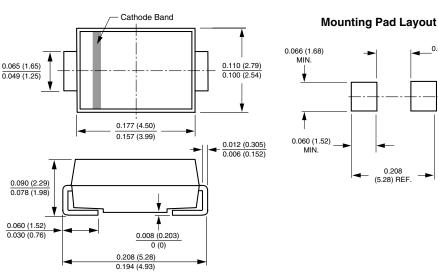


Figure 6. Typical Transient Thermal Impedance

0.074 (1.88)

MAX.



For technical questions within your region, please contact one of the following: PDD-Americas@vishay.com, PDD-Asia@vishay.com, PDD-Europe@vishay.com



Vishay

# Disclaimer

All product specifications and data are subject to change without notice.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

Vishay disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications unless otherwise expressly indicated. Customers using or selling Vishay products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Vishay for any damages arising or resulting from such use or sale. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

Product names and markings noted herein may be trademarks of their respective owners.